

GW1AN-5K POWER SPEC

Note!

[1] It is recommended to set Bank VCCIO of True LVDS to 2.5V.

[2] VCCX should be greater than or equal to VCCIO.

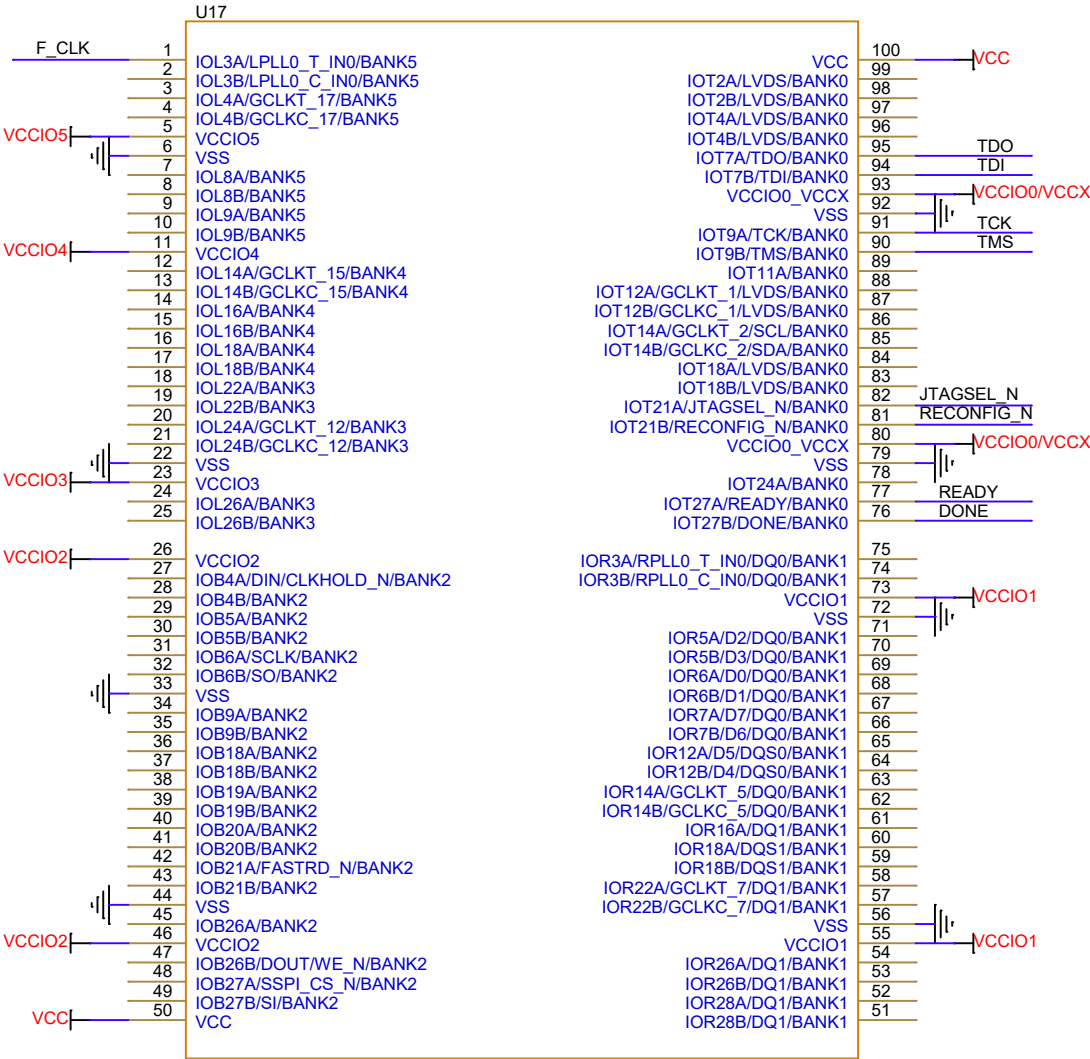
Name	Description	Min.	Max.
VCC	Core voltage (LV version)	1.14V	1.26V
	Core voltage (UV version)	2.25V	3.465V
VCCIO	I/O Bank voltage	1.14V	3.6V
	I/O Bank 0 voltage (supplies power to the built-in NOR Flash)	1.65V	3.465V
VCCX	Auxiliary voltage (LV version)	2.375V	3.465V
	Auxiliary voltage (UV version)	2.25V	3.465V
VEFUSE	Voltage required for eFuse writing	1.62V	1.98V
VCCADC	ADC module supply voltage	TBD	TBD
VREFN	ADC module negative reference voltage	TBD	TBD
VREFP	ADC module positive reference voltage	TBD	TBD

Notes:

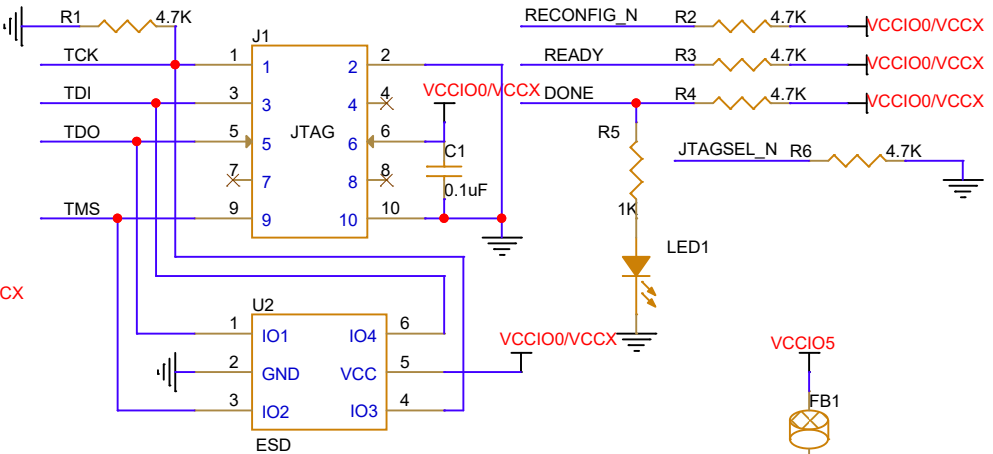
- 1、 It is recommended that add an ESD protection chip to the JTAG download circuit.
- 2、 VCCX cannot be less than the maximum VCCIO.
- 3、 After the FPGA is powered on or awakened, the MIO/MI1/MI2/MI3/MCLK/MCS_N pins support external MCU access to the SPI Flash;
After the FPGA is powered on or awakened, raising the MCS_N pin will enable MIO/MI1/MI2/MI3/MCLK to be reused as GPIO.

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A4	GW1AN-5K POWER SPEC	1.0
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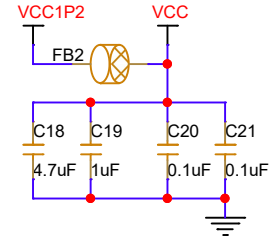
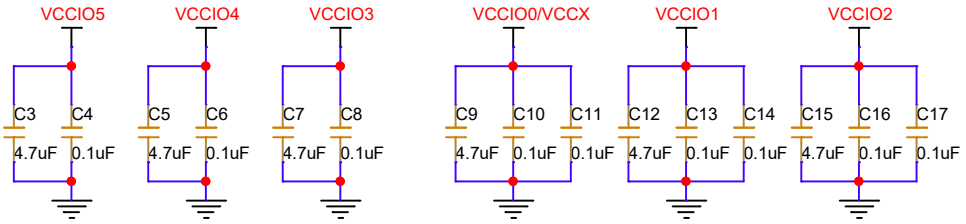
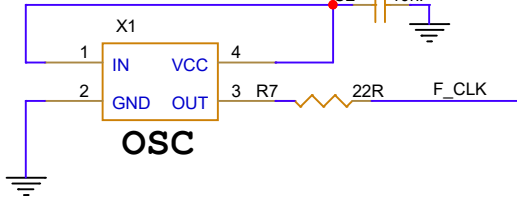
GW1AN-LV5LQ100X



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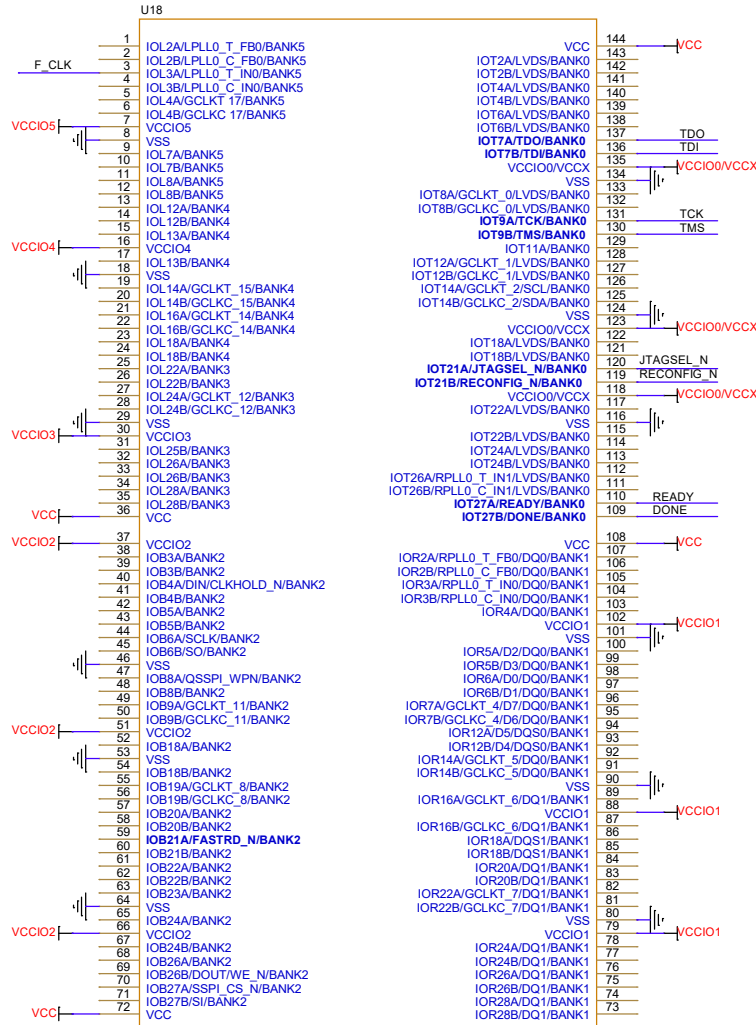


*JTAG download section

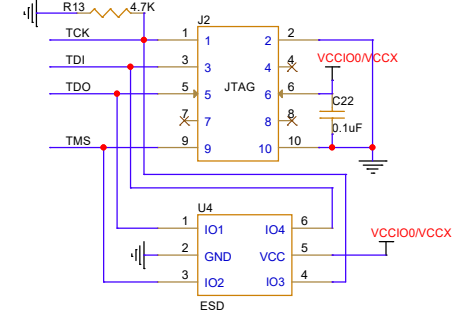
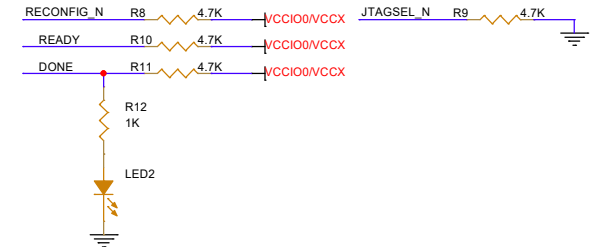


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A4	GW1AN-LV5LQ100X	1.0
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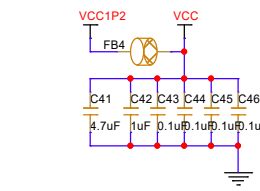
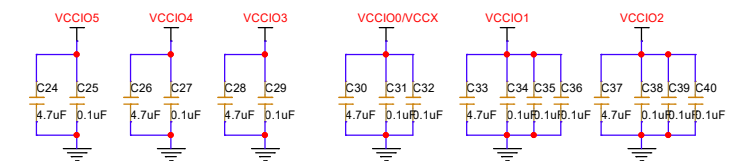
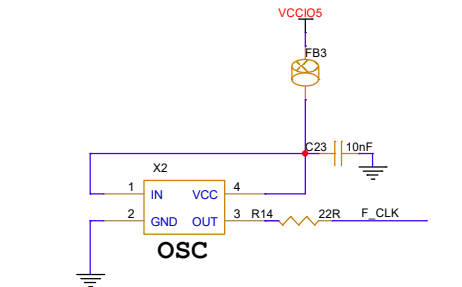
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GW1AN-LV5LQ144X

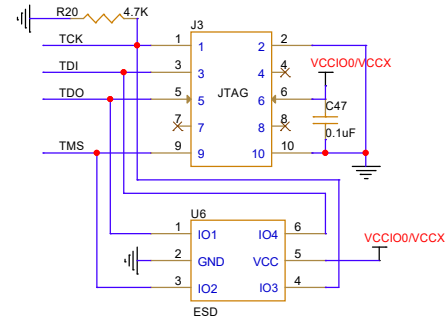
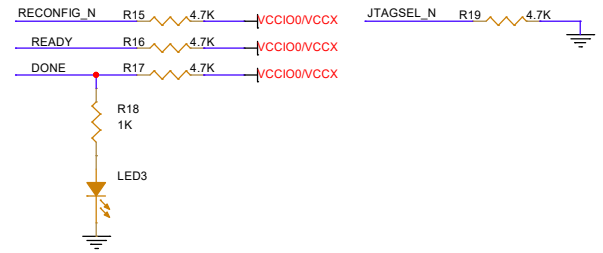
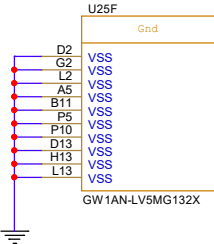
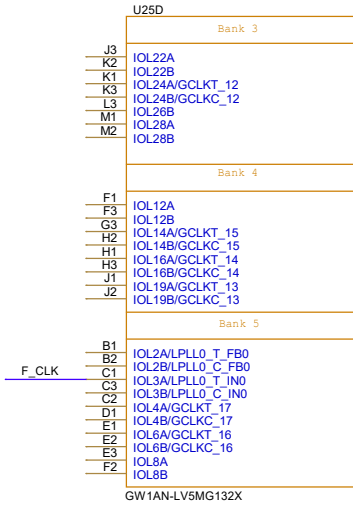
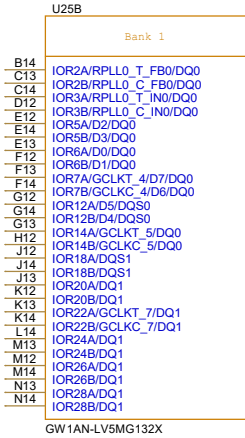
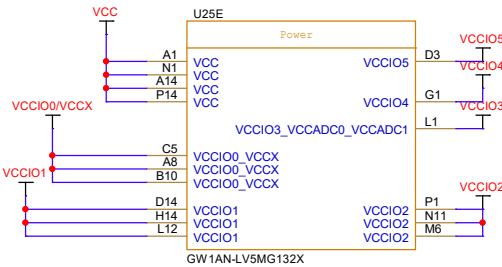
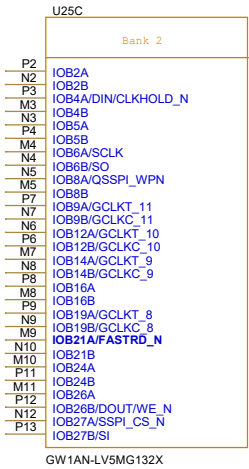
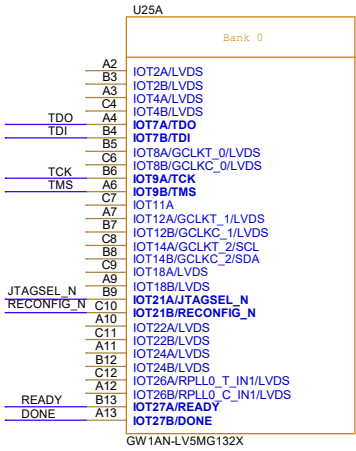


*JTAG download section

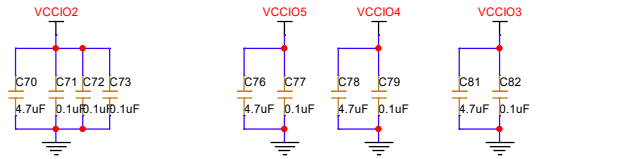
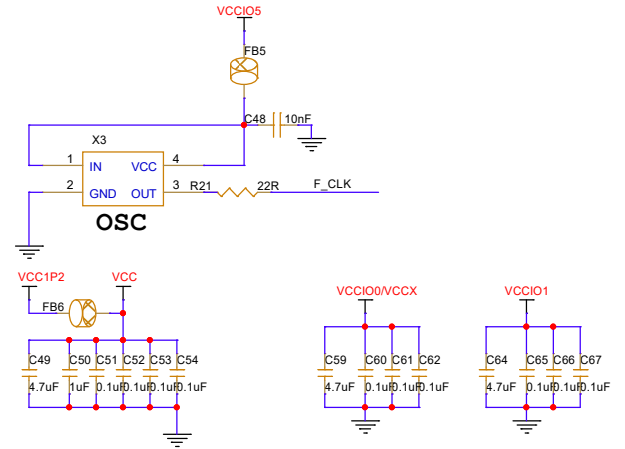


File			
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Size	Document Number	Rev	
A3	GW1AN-LV5LQ144X	1.0	
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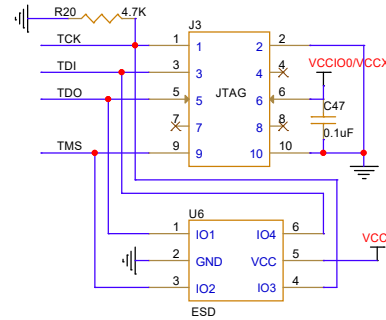
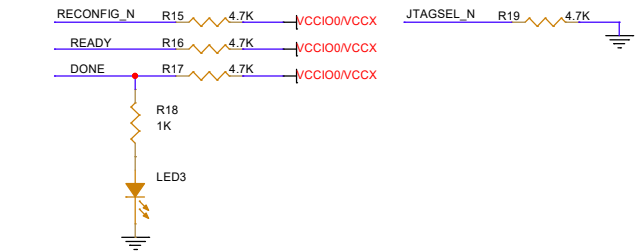
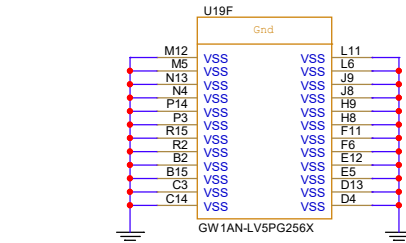
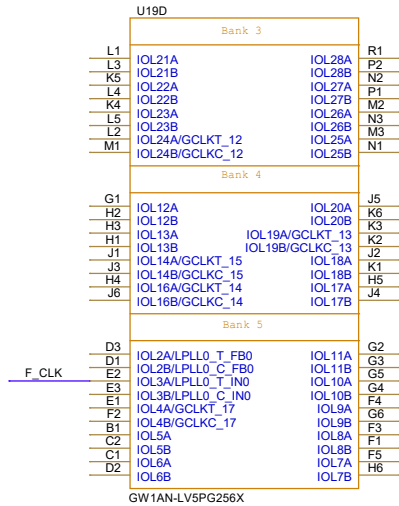
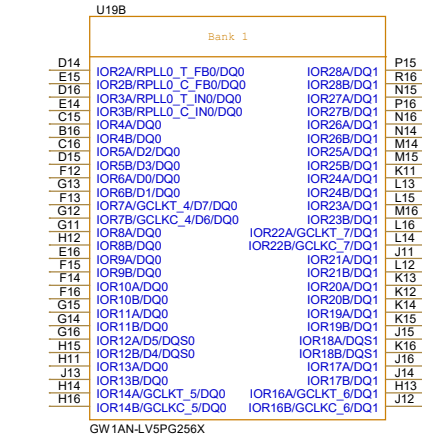
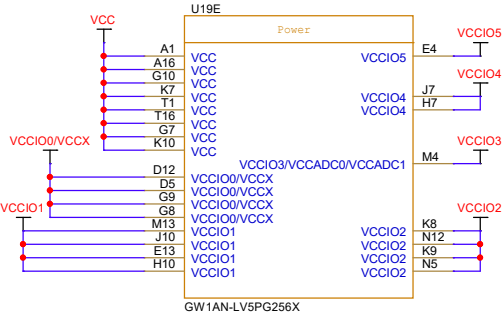
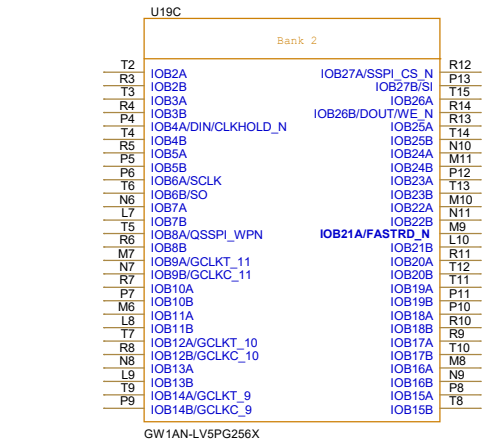
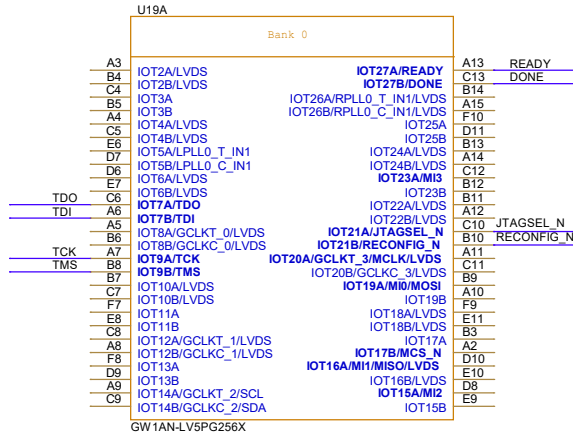


*JTAG download section

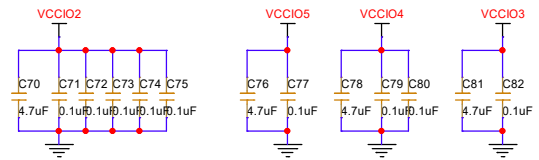
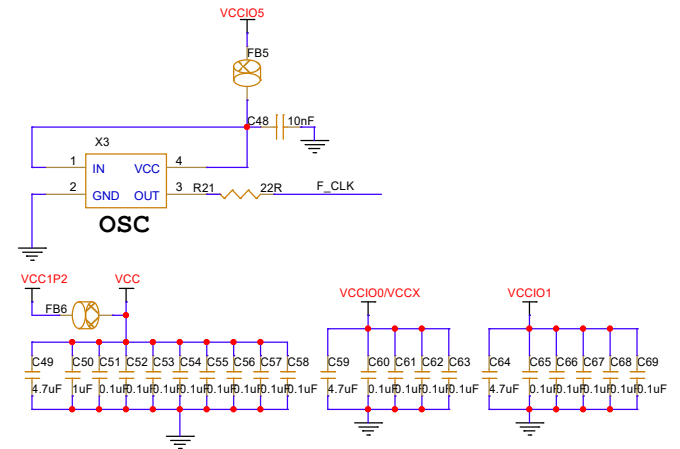


File			
GOWIN Minimum System SCH			
Size	Document Number	Rev	
A3	GW1AN-LV5MG132X	1.0	
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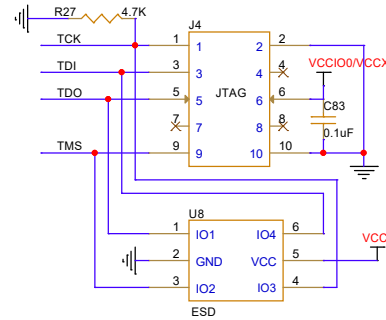
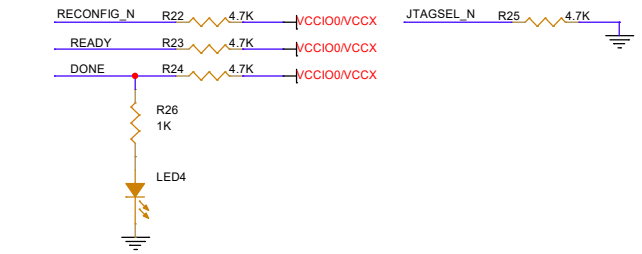
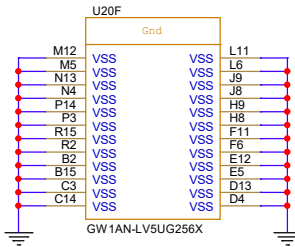
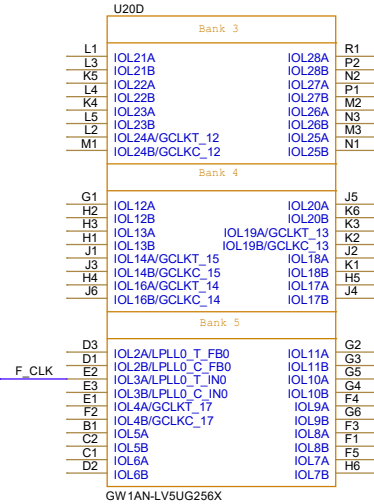
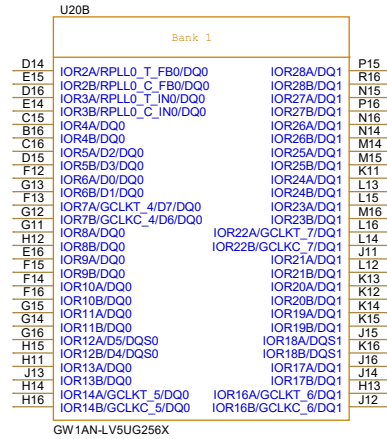
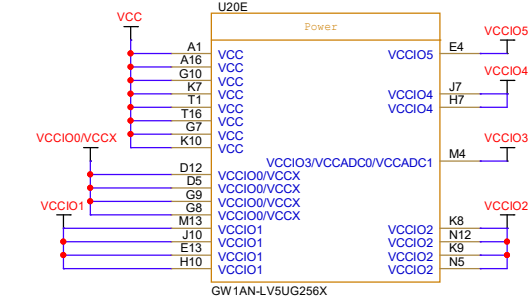
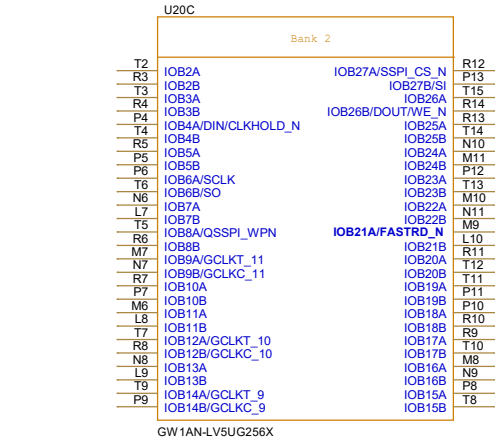
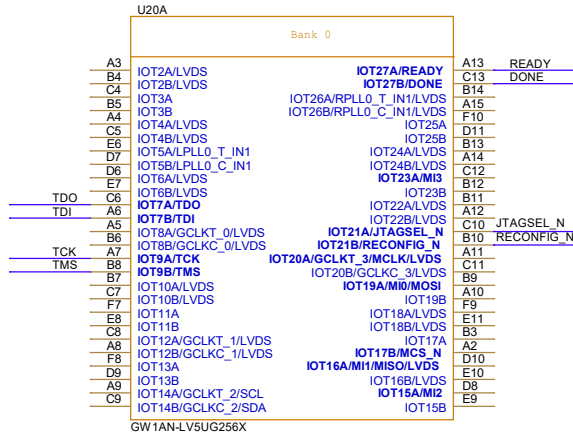
GW1AN-LV5PG256X



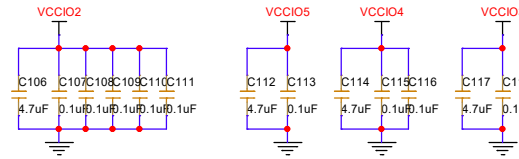
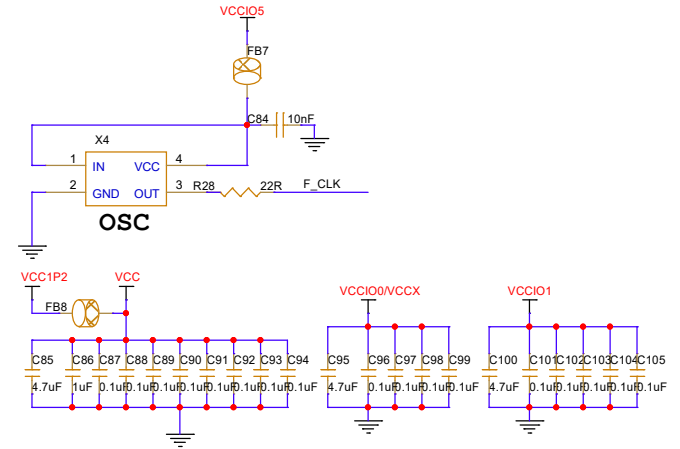
*JTAG download section



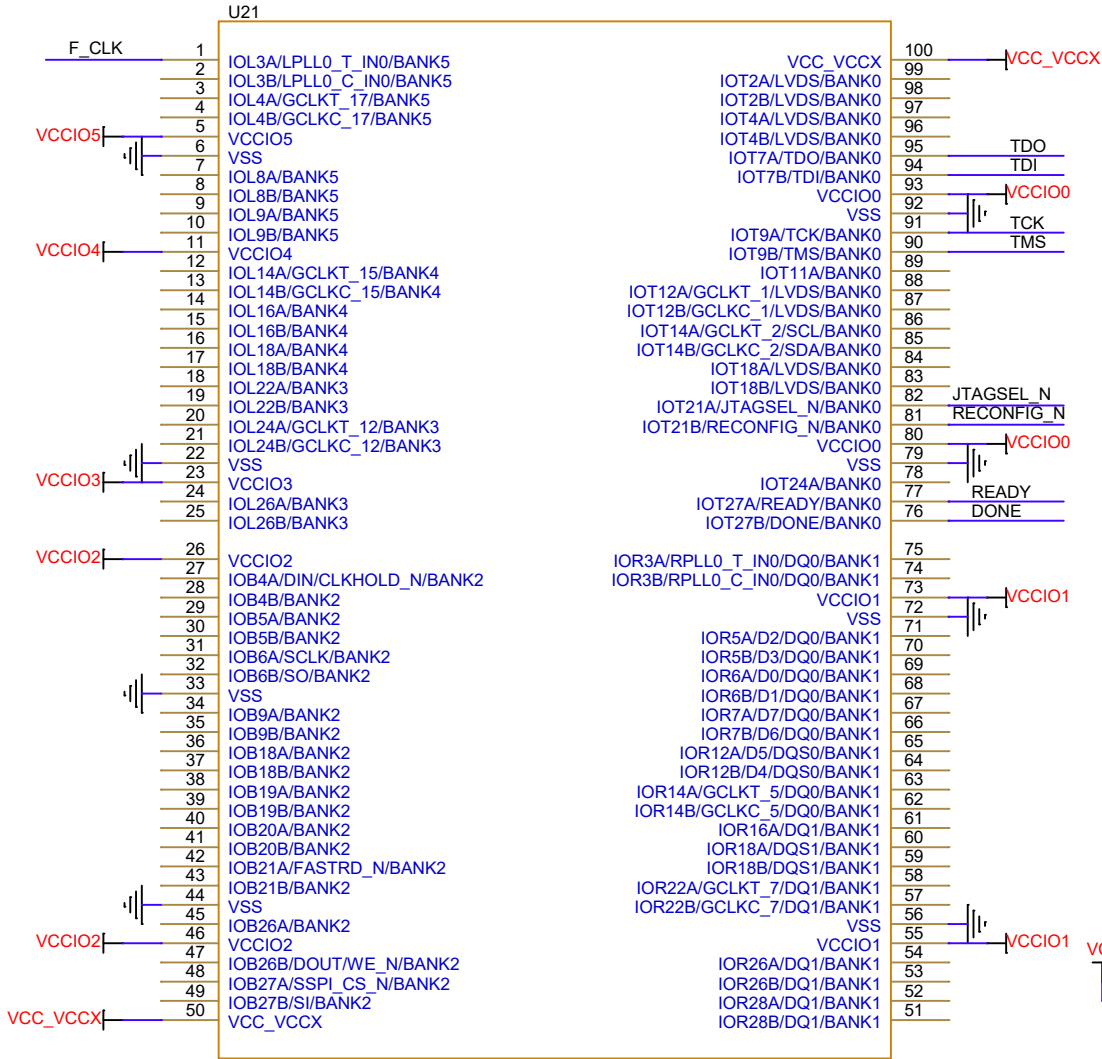
GW1AN-LV5UG256X



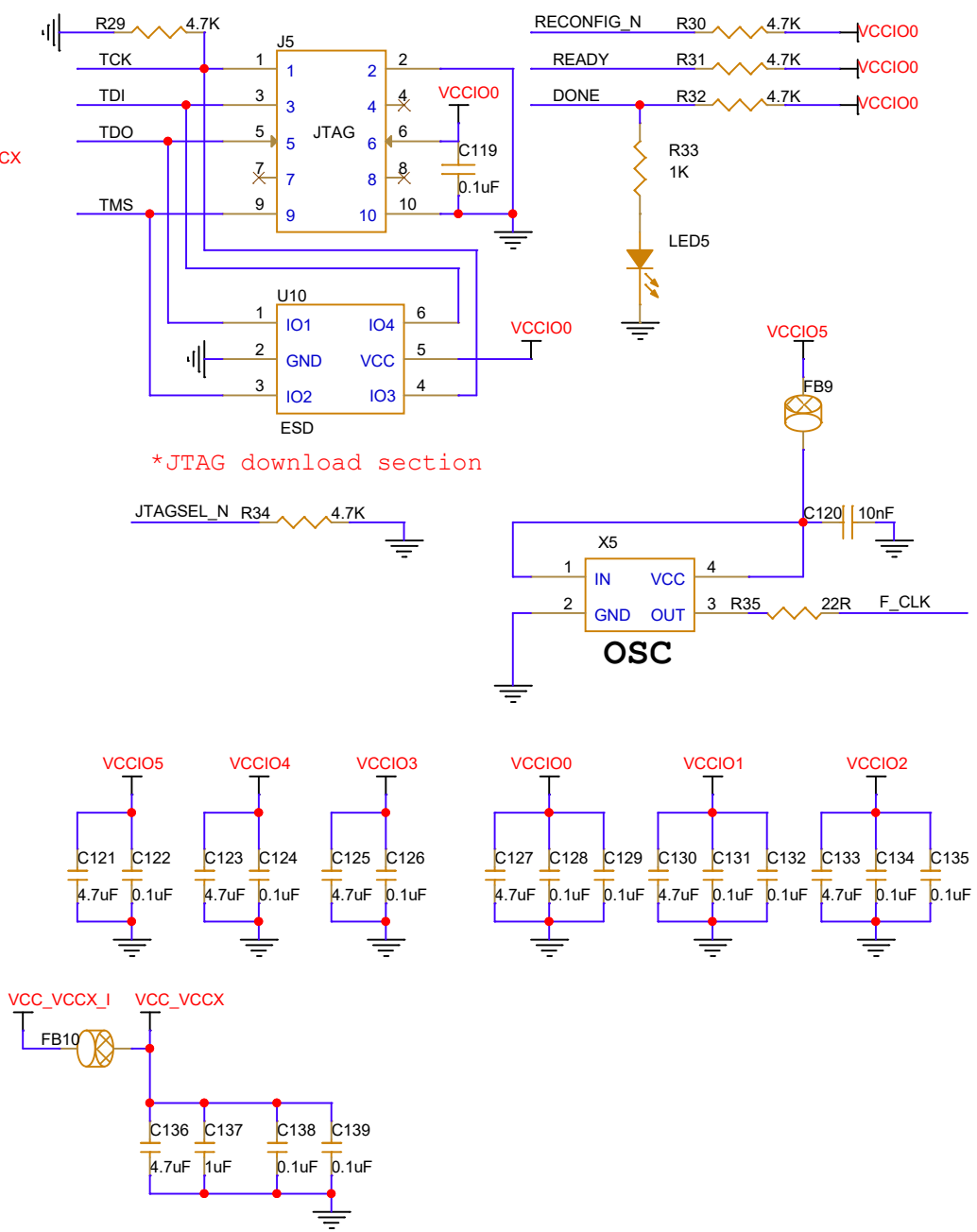
*JTAG download section



GW1AN-UV5LQ100X

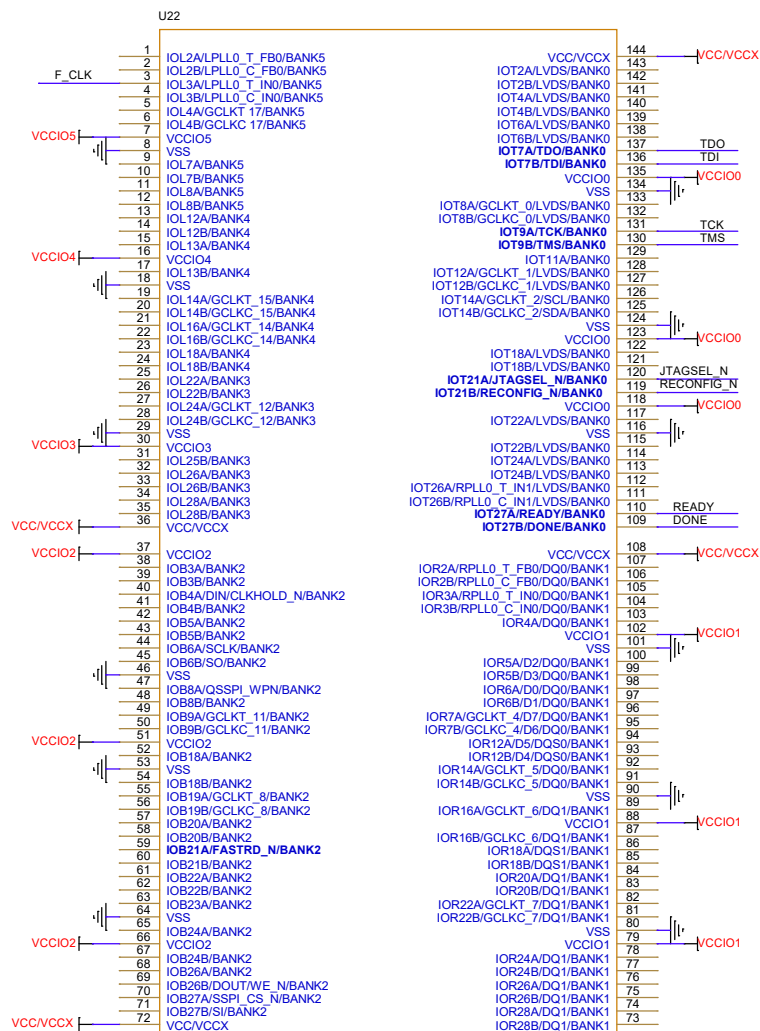


GW1AN-UV5LQ100X

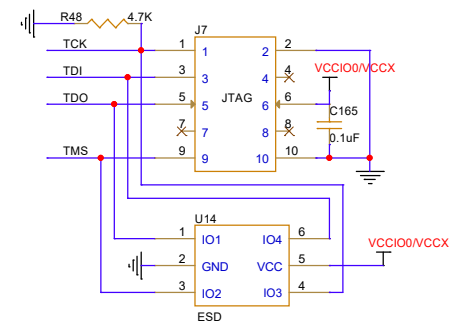
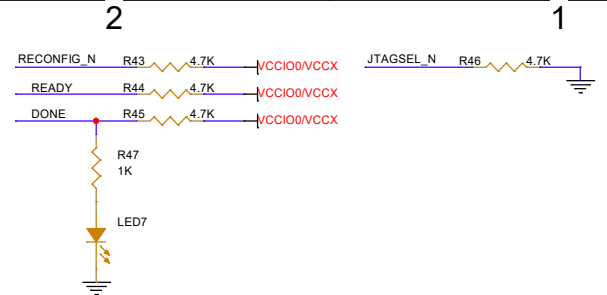
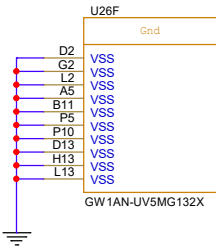
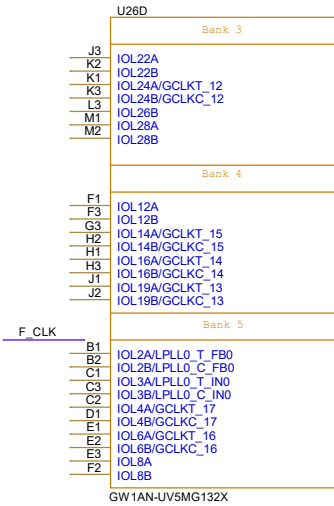
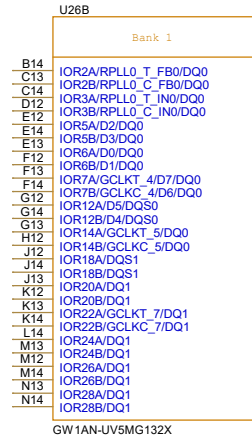
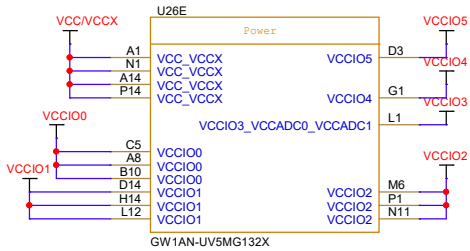
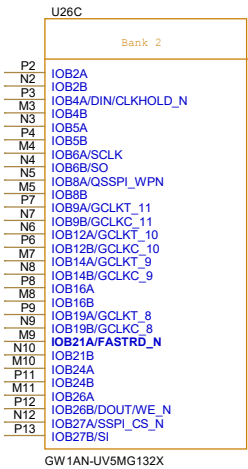
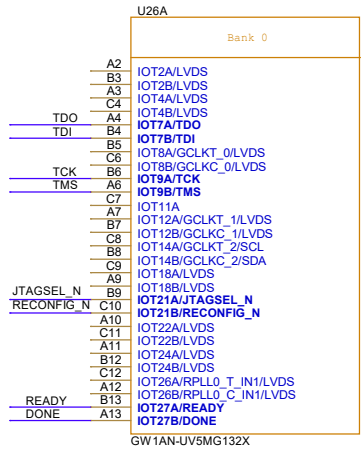


Title			
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Size	Document Number		Rev
A4	GW1AN-UV5LQ100X		1.0
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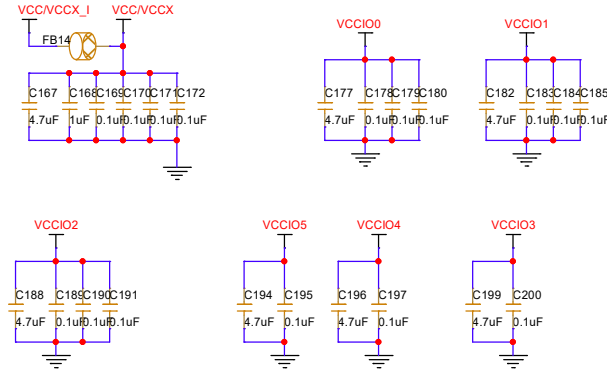
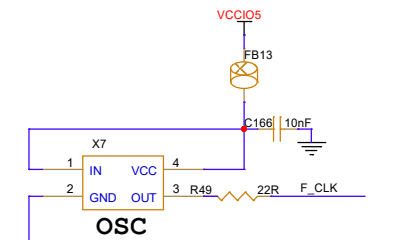
GW1AN-UV5LQ144X



GW1AN-UV5MG132X

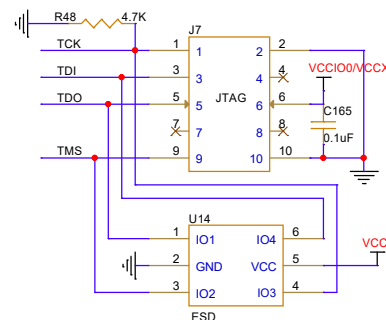
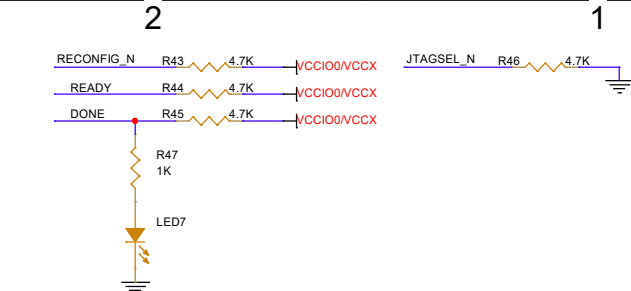
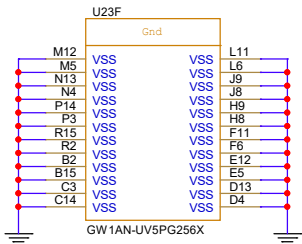
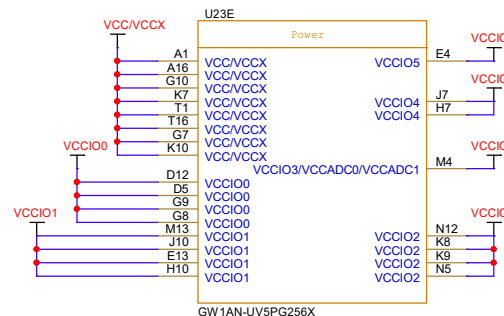
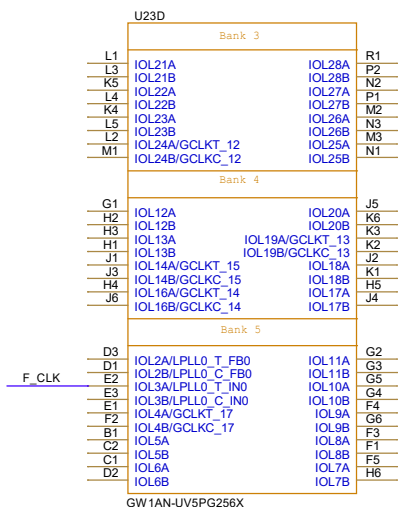
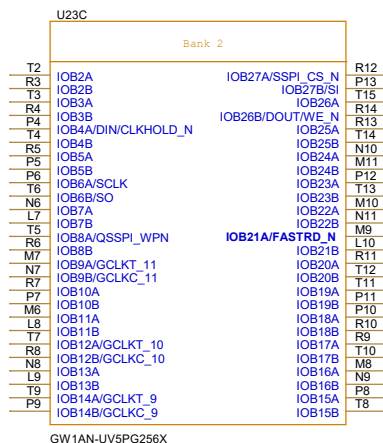
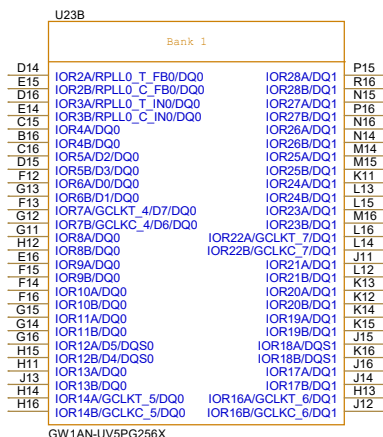
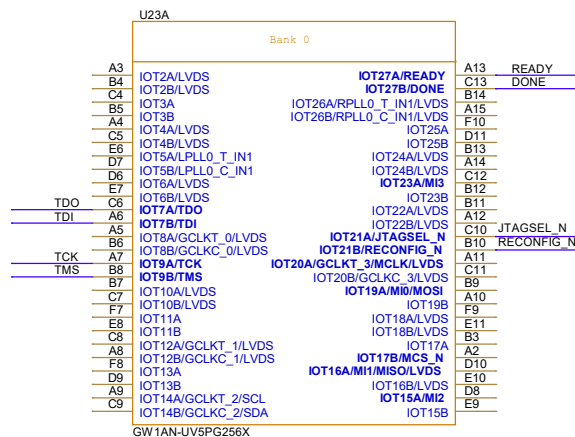


*JTAG download section

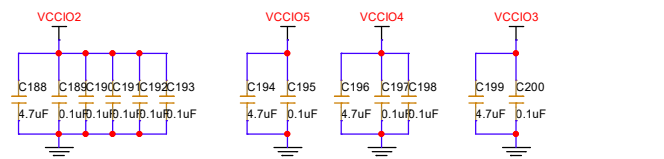
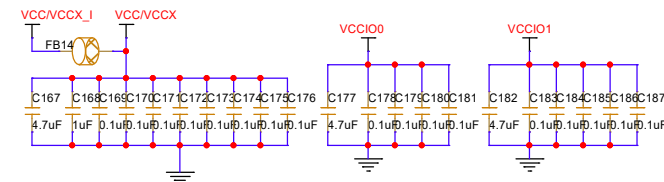
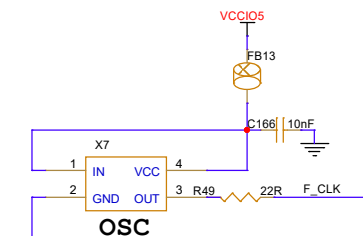


File			
GOWIN Minimum System SCH			
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A3	GW1AN-UV5MG132X	1.0	
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